

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re application of:

Applicant:

Theodore Vaida, et al.

Serial No.:

09/851,181

Filed:

May 8, 2001

Title:

APPLICATION SPECIFIC INTEGRATED CIRCUIT HAVING A

PROGRAMMABLE LOGIC CORE AND A METHOD OF OPERATION

**THEREOF** 

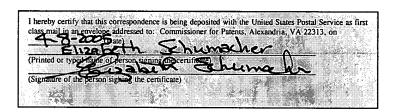
Grp./A.U.:

2661

Examiner:

Phan, Tri. H.

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450



Sir:

## **DRAWING AMENDMENT UNDER 37 C.F.R. § 1.121**

The attached sheets of drawings are formal drawings to replace the informal drawings initially filed with the above-identified application.

Attachment: Replacement Sheets: 1-6.